

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number : 10/595,303 Confirmation No.: 7114
Applicant : Roy KNECHTEL
Filed : April 6, 2006
Title : METHOD AND DEVICE FOR SECURE, INSULATED AND ELECTRICALLY CONDUCTIVE ASSEMBLING OF TREATED SEMICONDUCTOR WAFERS
TC/Art Unit : Unassigned
Examiner: : Unassigned

Docket No. : 60291.000048
Customer No. : 21967

STATUS INQUIRY

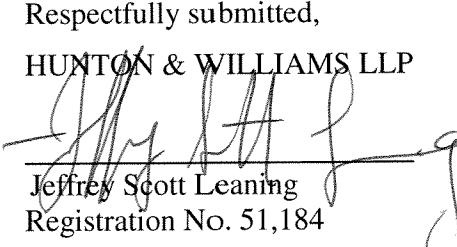
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

The above-identified patent application was filed on April 6, 2006. We last filed a Response to Notification of Missing Requirements on September 7, 2006. To date, however, Applicants have not received a Notification of Acceptance of Application Under 35 U.S.C. § 371 and 37 CFR 1.495 (Form PCT/DO/EO/903) and filing receipt.

Accordingly, Applicants respectfully request that the U.S. Patent and Trademark Office (“USPTO”) inform the undersigned representative as to when the Notification of Acceptance and filing receipt or a first communication on the merits from the USPTO will be received.

It is believed that no fee is due in connection with this filing. However, in the event that any fees are necessary, the Commissioner is hereby authorized to charge our Deposit Account No. 50-0206.

Dated: July 26, 2007
HUNTON & WILLIAMS LLP
1900 K Street, N.W., Suite 1200
Washington, D.C. 20006-1109
(202) 955-1500 (Telephone)
(202) 778-2201 (Facsimile)

Respectfully submitted,
HUNTON & WILLIAMS LLP
By: 
Jeffrey Scott Leaning
Registration No. 51,184